

55. The electric device according to claim 51 wherein said semiconductor integrated circuit chip is connected to a wiring comprising indium tin oxide formed over said insulating substrate.--

REMARKS

A Continuing Prosecution Application is filed herewith together with an Information Disclosure Statement and Preliminary Amendment. The specification has been amended herewith to withdraw reference to the claim for priority to U.S. Application Serial Number 08/217,211. In addition, new claims 51-55 have been added to recite additional protection to which the Applicants are entitled.

In paragraph 1 of the Office Action, claims 1-7, 9, 10, 17-24, 26, 27, 30-50 were rejected under the judicially created doctrine of double patenting over claims 1, 3, 12, 13 and 17 of U.S. Patent No. 5,889,291. Applicants respectfully traverse this rejection for the following reasons.

The Applicants respectfully submit that the '291 Patent does not fully disclose and cover the subject matter claimed in the present invention. For example, in the present invention, claims 1, 7, 17, 21, and 38 are directed to a semiconductor integrated circuit chip having at least one of a memory, an input port, a correction memory, and a CPU. In addition, claims 1, 7, 21, 32, 36, and 38 claim that at least one thin film transistor and at least another one thin film transistor are formed from a common semiconductor film formed over the first surface of the insulating substrate. Claims 7 and 32 claim that at least one thin film transistor

of the active matrix circuit has at least one lightly doped drain between a channel region and a drain region thereof while claim 36 claims that each of said first plurality of thin film transistors is a bottom gate type transistor, and each of said second plurality of thin film transistors is a top gate type transistor. The Applicants respectfully submit that none of these limitations are claimed in '291 Patent. Consequently, the Applicants believe that the present invention is patentably distinct over the '291 Patent, and that the double patenting rejection is improper.

Respectfully submitted,

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